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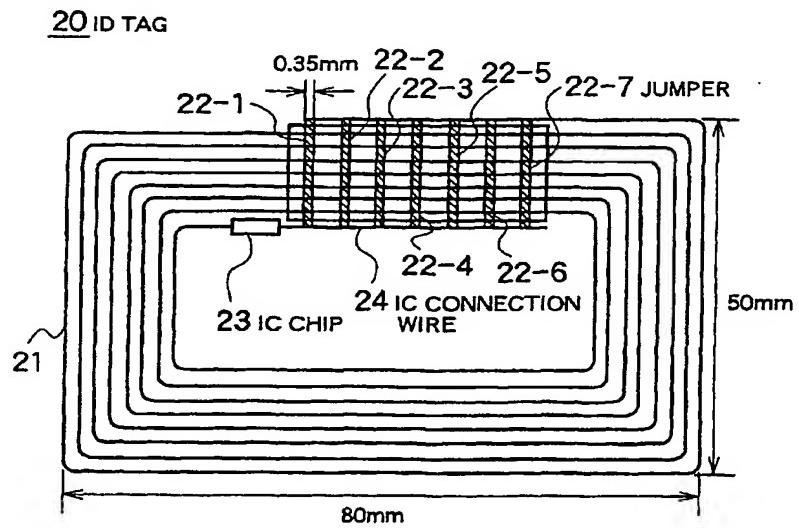
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### (54) Flat coil component, characteristic adjusting method of flat coil component, id tag, and characteristic adjusting method of id tag

(57) To present the ID tag including flat coil component, and characteristic adjusting method of ID tag capable of suppressing product fluctuations about the desired characteristic. The flat coil component of the ID tag of the invention comprises a flat coil composed of a conductive material provided continuously and spirally on an insulating substrate, and a jumper disposed on the

flat coil with insulation, from one of inner end or outer end of this flat coil to the outside or inside of the flat coil where other end is positioned, in which the jumper is composed of a plurality of jumpers variable in the number of pieces in arrangement. The characteristic is adjusted by varying the number of effective pieces for forming the parallel arrangement.

FIG.1



**Description****BACKGROUND OF THE INVENTION****Field of the Invention**

[0001] The present invention relates to a flat coil component, a characteristic adjusting method of the flat coil component, an ID tag, and an characteristic adjusting method of the ID tag.

**Description of the Related Art**

[0002] Recently, in various processings such as individual verification, merchandise management, and distribution process, ID tags (also known as IC cards) have come to be used. In particular, non-contact type ID tags are used widely.

[0003] Non-contact type ID tags are operated either by internal batteries, or by energy of radio wave or magnetic flux from a reading device of ID tags, and in the latter case, for example, necessary energy is obtained by utilizing the function of a flat coil component or an LC resonance circuit, using a flat coil component as an antenna element (Japanese Patent Laid-open No. 11-259615, Japanese Patent Publication No. 2814477).

[0004] Fig. 8 explains an example of antenna pattern in a conventional non-contact type ID tag 10.

[0005] In a plan in Fig. 8, the ID tag 10 has a flat coil 11, a jumper 12, an IC chip 13, and an IC connection wire 14 on an insulating substrate 15.

[0006] The flat coil 11 has a function of an antenna, and in Fig. 8 a conductive pattern made of conductive material such as aluminum foil, copper foil or silver paste is spirally wound and formed in a rectangular shape. In the case of Fig. 8, the end portion positioned at the inside of the spiral form of the flat coil 11 is directly connected to one terminal of the IC chip 13. The end portion positioned at the outside of the spiral form of the flat coil 11 is connected to one end of the jumper 12.

[0007] The jumper 12 is designed to transmit the electric characteristic (for example, potential) at the outside end portion of the flat coil 11 to the inside of the spiral form of the flat coil 11, and it is insulated from each loop of the flat coil 11. The jumper 12 is composed of a conductive layer 12A and an insulating layer 12B as described below (see Fig. 9).

[0008] The IC connection wire 14 is connected to the inside end portion of the jumper 12 and the other terminal of the IC chip 13.

[0009] In the IC chip 13, the unique code of the ID tag and other data are stored, and the stored data is transmitted by receiving a reading signal from a reading device not shown (also known as ID tag reader). The energy necessary for such transmitting and receiving process by the IC chip 13 is obtained mainly from the flat coil 11.

[0010] Fig. 9 is a magnified sectional view of intersec-

tion of a certain loop of the flat coil 11 and the jumper 12.

[0011] In Fig. 9, on the insulating substrate 15 made of plastic resin such as polyethylene terephthalate or polycarbonate, the flat coil 11 made of copper foil or the like is provided, and the jumper 12 is provided on the flat coil 11. The jumper 12 is, in the case of Fig. 9, composed of an insulating layer 12B made of an insulating resist resin responsible for the insulating function from the flat coil 11, and a conductive layer 12A of silver paste or the like responsible for the conductive function.

[0012] Thus, at the intersection of a certain loop of the flat coil 11 and the jumper 12, since the insulating layer 12B is enclosed by two conductive layers (flat coil 11 and conductive layer 12A), it functions as a capacitor.

[0013] The capacitance component CB by this capacitor is determined by the distance between two conductive layers (flat coil 11 and conductive layer 12A), in other words, the thickness D of the insulating layer 12B, the specific inductive capacity  $\epsilon_r$  of the insulating layer 12B, and the overlapping area S of the loop of the flat coil 11 and conductive layer 12A as shown in Fig. 10, and it can be expressed in formula (1), where  $\epsilon_0$  is the dielectric constant of air.

$$CB = \epsilon_0 \cdot \epsilon_r \cdot S/D \quad (1)$$

[0014] In the case of Fig. 8, since the number of intersections of the flat coil 11 and jumper 12 is seven, it is equivalent that there are seven capacitors, and the combined capacitance of the capacitor components of these seven capacitors is the capacitance C of the jumper 12.

[0015] Herein, supposing the capacitance of the IC chip 13 to be  $C_{IC}$ , the resonance frequency F in Fig. 8 can be expressed in formula (2), where L is the inductor value due to the flat coil 11.

$$F = 1/2\pi\sqrt{L \cdot (C_{IC} + C)} \quad (2)$$

[0016] That is, by making use of the LC resonance, the energy necessary for the transmitting and receiving process by the IC chip 13 is obtained. The resonance frequency F is the exchange frequency between the ID tag 10 and the reading device (not shown).

[0017] In the conventional non-contact type ID tag, however, fluctuations of resonance frequency are significant in individual products.

[0018] The jumper 12 occupies a very small area as seen from the entire substrate area, and therefore, generally, the insulating layer 12B is first formed by applying the insulating resist resin, and then the conductive layer 12A is formed by silk printing of silver paste or the like.

[0019] However, when the insulating layer 12B is formed by coating, the precision of the thickness (that is, D) is prone to decline (fluctuations are large).

[0020] When the precision of the distance D is lower (fluctuations are larger), the precision of the capacitance

of the jumper 12 having its effect is forced to be lower (product fluctuations are increased), and the resonance frequency F is deviated from the desired frequency in many ID tags 10.

[0021] In the case of an ID tag 10 large in error of the resonance frequency from the desired resonance frequency F, the electric power receiving efficiency is lowered, and the communication distance with the reading device becomes shorter.

[0022] The invention is devised in the light of such problems, and it is an object thereof to present a flat coil component, characteristic adjusting method of flat coil component, ID tag, and characteristic adjusting method of ID tag, capable of suppressing product fluctuations of desired characteristics.

#### SUMMARY OF THE INVENTION

[0023] To solve the problems, the flat coil component of the first invention comprises a flat coil composed of a conductive material provided continuously and spirally on an insulating substrate, and a jumper disposed on the flat coil with insulation, from one of inner end or outer end of this flat coil to the outside or inside of the flat coil where other end is positioned, in which the jumper is composed of a plurality of jumpers variable in the number of pieces in arrangement.

[0024] The flat coil component relates to the flat coil component of the first invention, in which at least one of the plurality of jumpers is excluded from the number of jumpers in arrangement by changing from the conductive state to nonconductive state.

[0025] The flat coil component relates to the flat coil component of the first invention, in which at least one of the plurality of jumpers has electrical releasing portions missing the conductive material, and are incorporated into the number of pieces in arrangement by covering a conductive material to these electrical releasing portions.

[0026] The characteristic adjusting method of the flat coil component of the second invention comprises the flat coil composed of the conductive material provided continuously and spirally on an insulating substrate, and the jumper disposed on the flat coil with insulation, from one of inner end or outer end of this flat coil to the outside or inside of the flat coil where other end is positioned, in which the jumper is composed of a plurality of jumpers variable in the number of pieces in arrangement, and the characteristic is adjusted by varying the effective number of pieces in arrangement out of the plurality of jumpers.

[0027] The characteristic adjusting method of the flat coil component relates to the characteristic adjusting method of the flat coil component of the second invention, in which at least one of the plurality of jumpers is excluded from the number of jumpers in arrangement by changing from the conductive state to nonconductive state, and thereby the characteristic of the flat coil com-

ponent is adjusted.

[0028] The characteristic adjusting method of the flat coil component relates to the characteristic adjusting method of the flat coil component of the second invention, in which at least one of the plurality of jumpers has electrical releasing portions missing the conductive material, and are incorporated into the number of pieces in arrangement by covering a conductive material to these electrical releasing portions, and thereby the characteristic of the flat coil component is adjusted.

[0029] The ID tag of the third invention is an ID tag of non-contact type including a flat coil component as an antenna element, in which the flat coil component comprises a flat coil composed of a conductive material provided continuously and spirally on an insulating substrate, and a jumper disposed on the flat coil with insulation, from one of inner end or outer end of this flat coil to the outside or inside of the flat coil where other end is positioned, and further the jumper is composed of a plurality of jumpers variable in the number of pieces in arrangement.

[0030] The ID tag relates to the ID tag of the third invention, in which at least one of the plurality of jumpers is excluded from the number of jumpers in arrangement by changing from the conductive state to nonconductive state.

[0031] The ID tag relates to the ID tag of the third invention, in which at least one of the plurality of jumpers has electrical releasing portions missing the conductive material, and are incorporated into the number of pieces in arrangement by covering the conductive material to these electrical releasing portions.

[0032] The characteristic adjusting method of ID tag of the fourth invention is a characteristic adjusting method of ID tag of non-contact type including a flat coil component as an antenna element, in which the flat coil component comprises a flat coil composed of a conductive material provided continuously and spirally on an insulating substrate, and a jumper disposed on the flat coil with insulation, from one of inner end or outer end of this flat coil to the outside or inside of the flat coil where other end is positioned, and further the jumper is composed of a plurality of jumpers variable in the number of pieces in arrangement, and the characteristic of the ID tag is adjusted by varying the effective number of pieces in arrangement out of the plurality of jumpers.

[0033] The characteristic adjusting method of ID tag relates to the characteristic adjusting method of ID tag of the fourth invention, in which at least one of the plurality of jumpers is excluded from the number of jumpers in arrangement by changing from the conductive state to nonconductive state, and thereby the characteristic of the ID tag is adjusted.

[0034] The characteristic adjusting method of ID tag relates to the characteristic adjusting method of ID tag of the fourth invention, in which at least one of the plurality of jumpers has electrical releasing portions missing the conductive material, and are incorporated into

the number of pieces in arrangement by covering the conductive material to these electrical releasing portions, and thereby the characteristic of the ID tag is adjusted.

#### BRIEF DESCRIPTION OF THE DRAWINGS

[0035] Fig. 1 is a plan showing circuit elements and the like of the ID tag in a first embodiment.

[0036] Fig. 2 is a sectional view along the longitudinal direction of jumper in the first embodiment.

[0037] Fig. 3 is an explanatory diagram showing the characteristic adjusting procedure of ID tag in the first embodiment.

[0038] Fig. 4 is an explanatory diagram showing the relation between the number of effective jumpers and the design resonance frequency in the first embodiment.

[0039] Fig. 5 is an explanatory diagram of effects of ID tag in the first embodiment.

[0040] Fig. 6 is a schematic plan showing an essential configuration of ID tag in a second embodiment.

[0041] Fig. 7 is an explanatory diagram showing the characteristic adjusting procedure of ID tag in the second embodiment.

[0042] Fig. 8 is a plan showing circuit elements and others of a conventional ID tag.

[0043] Fig. 9 is a sectional view of intersection of jumper of conventional ID tag and flat coil.

[0044] Fig. 10 is an explanatory diagram of essential parts for determining the capacitance value at the intersection in Fig. 9.

#### DESCRIPTION OF THE PREFERRED EMBODIMENTS

##### (A) A first embodiment

[0045] Referring now to the drawings, a first embodiment of a flat coil component, a characteristic adjusting method of flat coil component, ID tag, and characteristic adjusting method of ID tag according to the invention will be explained below.

##### (A-1) Configuration of the first embodiment

[0046] Fig. 1 is an explanatory diagram of non-contact type ID tag using a flat coil component of the first embodiment, showing a schematic plan of a state in which the surface side of the ID tag is not covered, that is, the internal electric wiring pattern is formed and exposed directly.

[0047] In Fig. 1, an ID tag 20 of the first embodiment also comprises a flat coil 21, jumpers 22-1 to 22-7, an IC chip 23, and an IC connection wire 24.

[0048] The flat coil 21, IC chip 23, and IC connection wire 24 function same as in the prior art.

[0049] In the case of the first embodiment, a plurality of (seven pieces in Fig. 1) jumpers 22-1 to 22-7 are pro-

vided, and these jumpers 22-1 to 22-7 are arranged parallel between the outermost loop of the flat coil 21 and the IC connection wire 24.

[0050] In the jumpers 22-1, ..., 22-7, the wire width of the conductive layer (see reference numeral 22A in Fig. 2) is same in each wire, and is formed narrower than the wire width of the conductive layer formed of one jumper as in the prior art. For example, supposing the wire width of conductive layer 22A in the jumpers 22-1, ..., 22-7 to be  $t$ , and the wire width of the conductive layer formed of one jumper as in the prior art intended to be of same resonance frequency to be  $T$ , it is designed to meet the relation of  $7t > T$ .

[0051] The combined capacitance value in the parallel circuit about capacitance components is, as known well, the sum of capacitance values of each capacitance component. Therefore, when the wire width of the conductive layer 22A in the jumpers 22-1, ..., 22-7 is equal, and when the capacitance values are assumed to be equal in the jumpers 22-1, ..., 22-7, the combined capacitance value can be varied depending on the number of effective parallel pieces.

[0052] The jumpers 22-1, ..., 22-7 are designed so as to be removed from the number of effective parallel pieces easily.

[0053] Fig. 2 is a sectional view along the longitudinal direction of a certain jumper 22 (any one of 22-1 to 22-7).

[0054] In Fig. 2, the flat coil 21 and IC connection wire 24 are disposed on the insulating substrate 25 which is a basic substrate of the ID tag 20 by way of a resin layer 26. The resin layer 26 is an adhesive layer or the like used for providing the flat coil 21 on the insulating substrate 25. The flat coil 21 may be also disposed directly on the insulating substrate 25 without using resin layer 26.

[0055] On the conductive pattern of each loop excluding the outermost loop of the flat coil 21, the insulating layer (or a dielectric layer from the viewpoint of a capacitor) 22B of the jumper 22 is disposed, on which a conductive layer 22A of this jumper 22 is mounted. This conductive layer 22A is connected to the outermost loop of the flat coil 21 and the IC connection wire 24.

[0056] For example, when the conductive layer 22A of the jumper 22 (22-1, ..., 22-7) is composed of silver paste, by removing a part or plural parts of the silver paste (for example, the silver paste at the position of CT in Fig. 2) by using a needle-like tool, that is, by disconnecting the jumper, the specific jumper 22 can be removed from the parallel circuit.

[0057] Or, in the case of the jumper 22 (22-1, ..., 22-7) is composed like a conductive adhesive tape by using an adhesive layer as the insulating layer 22B and a metal tape as the conductive layer 22A, by peeling off the entire jumper, it can be removed from the parallel circuit.

(A-2) Characteristic adjusting method of the first embodiment

[0058] The characteristic adjusting method of the ID tag 20 (also used for the characteristic adjusting method of the flat coil component) in the first embodiment composed of such circuit elements is explained by referring to Fig. 3 which shows the processing procedure.

[0059] The characteristic adjustment shown in Fig. 3 may be done automatically, or part or whole of the procedure may be carried out by the adjusting operator.

[0060] First, the resonance frequency is detected in the ID tag 20 in the processed state except for surface coating step as shown in Fig. 1 (T1).

[0061] Then, the error between the desired resonance frequency of the ID tag 20 and the detected resonance frequency is checked (T2), and it is determined if the error is within a specified range or not (T3).

[0062] When the error is within the specified range, the series of processing shown in Fig. 3 is terminated. At this time, the process proceeds to the surface coating, so that the ID tag is completed.

[0063] By contrast, if the detected error of resonance frequency exceeds the specified range, it is determined if adjustable by decreasing the number of jumpers or not (T4). For example, if the error is too large, it is determined as not adjustable. Or in the case if there is only one jumper with no breakage or peeling left, it is determined as not adjustable since no effective jumper is present by removing the jumper.

[0064] If adjustable by decreasing the number of jumpers, one of them is removed from the parallel circuit (for example, by breaking process or peeling process) (T5), and the process returns to step T1.

[0065] On the other hand, if impossible to adjust by decreasing the number of jumpers, the process for defective piece is performed by noticing a defective piece or the like (T6), and the series of process shown in Fig. 3 is terminated.

(A-3) Effects of the first embodiment

[0066] As described above, according to the first embodiment, a plurality of jumpers are provided so as to allow to vary the number of pieces for composing the parallel circuit, and the number of pieces is varied appropriately, and therefore the error between the resonance frequency of the ID tag and the desired resonance frequency can be suppressed, so that the communication distance from the reading device may be extended.

[0067] The experiment conducted herein will be explained below. The ID tag 20 of the first embodiment having the configuration as shown in Fig. 1 and Fig. 2 was composed of the following materials.

[0068] Insulating substrate 25: A polyethylene terephthalate film of 50 µm in thickness.

[0069] Flat coil 21 and IC connection wire 24: A cop-

per foil of 35 µm in thickness having an adhesive layer of 20 µm in thickness on one side was adhered to the insulating substrate 25, and the wire width was adjusted to 0.8 mm by etching.

[0070] IC chip 23: IC chip for RFID (trade name as I-CODE of Philips Inc.).

[0071] Insulating layer 22B of jumper: An acrylic resin insulating resist layer (target film thickness 25 µm) (trade name as ML25089 of Acheson Japan Limited.).

[0072] Conductive layer 22A of jumper: A silver paste layer of film thickness of 15 µm and wire width of 0.35 mm (trade name as DW250L-1 of Toyobo Co.).

[0073] In such design condition, a relation shown in Fig. 4 is established between the number of effective pieces of jumpers 22-1 to 22-7 and the resonance frequency. That is, when the number of effective pieces is five, the resonance frequency becomes as desired.

[0074] However, the thickness of insulating layer of jumper varies in manufacture. In the said design condition (target thickness of 25 µm of insulating layer, etc.), three samples were manufactured. Thickness of the insulating layer was 22.0 µm in the first sample, 26.4 µm in the second sample, and 26.6 µm in the third sample. Fig. 5 shows the resonance frequency before and after

the characteristic adjustment process of these samples.

[0075] In the first sample, when two out of seven jumpers were removed, the resonance frequency was 13.559 MHz, and the error from the desired resonance frequency of 13.56 MHz was -0.001 MHz.

[0076] In the second sample, when one out of seven jumpers was removed, the resonance frequency was 13.559 MHz, and the error from the desired resonance frequency of 13.56 MHz was -0.001 MHz. In the third sample, when one out of seven jumpers was removed, the resonance frequency was 13.568 MHz, and the error from the desired resonance frequency of 13.56 MHz was 0.008 MHz.

[0077] At the present, the error range is -0.098 to 0.057 MHz, and the error range in the first embodiment was -0.001 to 0.008 MHz, and it is known that the desired resonance frequency is almost achieved in the first embodiment.

(B) A second Embodiment

[0078] Referring now to the drawings, a second embodiment of flat coil component, characteristic adjusting method of flat coil component, ID tag, and characteristic adjusting method of ID tag according to the invention will be explained below.

(B-1) Configuration of the second embodiment

[0079] Fig. 6 is a schematic plan (image diagram) of essential parts of the second embodiment, and same parts corresponding to the first embodiment are identified with same reference numerals.

[0080] In the ID tag 20A of the second embodiment, the flat coil 21, IC chip 23, and IC connection wire 24

are same as the ones in the first embodiment.

**[0080]** The ID tag 20A of the second embodiment has jumpers 22A-1 to 22A-7 as shown in Fig. 6 before the characteristic adjustment process. The first to fifth jumpers 22A-1 to 22A-5 are same as the ones in the first embodiment.

**[0081]** The sixth and seventh jumpers 22A-6 and 22A-7 have notches 22-6X and 22-7X formed in part of the conductive layer (see 22A in Fig. 2). These notches 22-6X and 22-7X are covered with conductive material (for example, silver paste or solder), so that the entire jumpers 22A-6, 22A-7 are set in a conductive state, and such conductive state is incorporated into the parallel circuit composed of plural jumpers.

**[0082]** Herein, the number of jumpers 22A-1 to 22A-5 not having the notches is five, which is because the desired resonance frequency is achieved by five pieces in design.

#### (B-2) Characteristic adjusting method of the second embodiment

**[0083]** The characteristic adjusting method of the ID tag 20A (also used for the characteristic adjusting method of the flat coil component) in the second embodiment composed of such circuit elements will be explained by referring to Fig. 7 which shows the processing procedure.

**[0084]** The characteristic adjustment shown in Fig. 7 may be carried out automatically, or part or whole of the procedure may be carried out by the adjusting operator.

**[0085]** First, the resonance frequency is detected in the ID tag 20A in the state shown in Fig. 6 (T11).

**[0086]** Then, the error between the desired resonance frequency of the ID tag 20A and the detected resonance frequency is checked (T12), and it is determined if the error is within a specified range or not (T13).

**[0087]** When the error is within the specified range, the series of processing shown in Fig. 7 is terminated. At this time, the process proceeds to the surface coating, so that the ID tag 20A is completed.

**[0088]** By contrast, if the detected error of resonance frequency exceeds the specified range, it is determined if the error is plus or minus (T14).

**[0089]** In the case of a minus error, it is determined if adjustable by decreasing the number of effective jumpers or not (T15), and if adjustable by decreasing the number of effective jumpers, one of them is removed from the parallel circuit (for example, by breaking process or peeling process) (T16), and the process returns to step T11. On the other hand, if impossible to adjust by decreasing the number of jumpers, the process for defective piece is performed by noticing a defective piece or the like (T17), and the series of process shown in Fig. 7 is terminated.

**[0090]** In the case of a plus error, it is determined if adjustable by increasing the number of effective jumpers or not (T18), and if adjustable by increasing the

number of effective jumpers, one jumper is added to the parallel circuit (for example, by covering the notches with the conductive material) (T19), and the process returns to step T11. On the other hand, if impossible to adjust by increasing the number of jumpers, the process for defective piece is performed by noticing a defective piece or the like (T17), and the series of process shown in Fig. 7 is terminated.

#### 10 (B-3) Effects of the second embodiment

**[0091]** As described above, according to the second embodiment, a plurality of jumpers are provided so as to allow to vary the number of pieces for composing the parallel circuit, and the number of pieces is varied appropriately, and therefore the error between the resonance frequency of the ID tag and the desired resonance frequency can be suppressed, so that the communication distance from the reading device may be extended.

#### (C) Other embodiments

**[0092]** The materials of the flat coil, jumper and insulating substrate, and their pattern forming methods are not particularly specified, but may be selected arbitrarily.

**[0093]** In the foregoing embodiments, the IC chip 23 is positioned inside of the flat coil 21, but the invention may be also applied in a case in which the IC chip 23 is positioned outside of the flat coil 21 and the jumper conducts the inside end portion of the flat coil 21 to the outside of the flat coil.

**[0094]** In the embodiments, when adjusting the characteristic, the jumper is increased or decreased one by one, but considering the magnitude of error, two or more jumpers may be increased or decreased in one operation for adjusting the number of pieces.

**[0095]** Further in the embodiments, in all of ID tags 20, 20A, the resonance frequency is detected, and the number of parallel jumpers is adjusted (to adjust the characteristic), but it may be processed in the following product lot unit. That is, from the product lot (for example, 100 pieces) of ID tags 20, 20A manufactured at the same time and by the same machine that are assumed to have nearly uniform characteristics, a specified number (for example, three pieces) of ID tags 20, 20A are sampled, and from the sample number of ID tags 20, 20A, the number of pieces of increasing or decreasing the jumpers is determined, and the specified number of jumpers may be increased or decreased in all ID tags 20, 20A of the product lot.

**[0096]** In the embodiments, the flat coil component of the invention is applied to the ID tag, but the flat coil component of the invention can be applied widely in all flat electronic circuits containing flat coil components as elements, and the characteristic may be adjusted appropriately.

**[0097]** As described above, the invention realizes the

flat coil component, characteristic adjusting method of flat coil component, ID tag, and characteristic adjusting method of ID tag, capable of suppressing product fluctuations about the desired characteristic.

### Claims

1. A flat coil component comprising a flat coil composed of a conductive material provided continuously and spirally on an insulating substrate, and a jumper disposed on the flat coil with insulation, from one of inner end or outer end of this flat coil to the outside or inside of the flat coil where other end is positioned,  
wherein the jumper is composed of a plurality of jumpers variable in the number of pieces in arrangement.
2. The flat coil component of claim 1, wherein at least one of the plurality of jumpers is excluded from the number of jumpers in arrangement by changing from the conductive state to nonconductive state.
3. The flat coil component of claim 1, wherein at least one of the plurality of jumpers has electrical releasing portions missing the conductive material, and are incorporated into the number of pieces in arrangement by covering a conductive material to these electrical releasing portions.
4. A characteristic adjusting method of a flat coil component comprising a flat coil composed of a conductive material provided continuously and spirally on an insulating substrate, and a jumper disposed on the flat coil with insulation, from one of inner end or outer end of the flat coil to the outside or inside of the flat coil where other end is positioned,  
wherein the jumper is composed of a plurality of jumpers variable in the number of pieces in arrangement, and the characteristic is adjusted by varying the effective number of pieces in arrangement out of the plurality of jumpers.
5. The characteristic adjusting method of flat coil component of claim 4, wherein at least one of the plurality of jumpers is excluded from the number of jumpers in arrangement by changing from the conductive state to nonconductive state, and thereby the characteristic of the flat coil component is adjusted.
6. The characteristic adjusting method of flat coil component of claim 4, wherein at least one of the plurality of jumpers has electrical releasing portions missing the conductive material, and are incorporated into the number of pieces in arrangement by covering a conductive material to these electrical releasing portions, and thereby the characteristic of the flat coil component is adjusted.
7. An ID tag of non-contact type including a flat coil component as an antenna element,  
wherein the flat coil component comprises a flat coil composed of a conductive material provided continuously and spirally on an insulating substrate, and a jumper disposed on the flat coil with insulation, from one of inner end or outer end of the flat coil to the outside or inside of the flat coil where other end is positioned, and further  
the jumper is composed of a plurality of jumpers variable in the number of pieces in arrangement.
8. The ID tag of claim 7, wherein at least one of the plurality of jumpers is excluded from the number of jumpers in arrangement by changing from the conductive state to nonconductive state.
9. The ID tag of claim 7, wherein at least one of the plurality of jumpers has electrical releasing portions missing the conductive material, and are incorporated into the number of pieces in arrangement by covering the conductive material to these electrical releasing portions.
10. A characteristic adjusting method of an ID tag of non-contact type including a flat coil component as an antenna element,  
wherein the flat coil component comprises a flat coil composed of a conductive material provided continuously and spirally on an insulating substrate, and a jumper disposed on the flat coil with insulation, from one of inner end or outer end of this flat coil to the outside or inside of the flat coil where other end is positioned, and further  
the jumper is composed of a plurality of jumpers variable in the number of pieces in arrangement, and the characteristic of the ID tag is adjusted by varying the effective number of pieces in arrangement out of the plurality of jumpers.
11. The characteristic adjusting method of the ID tag of claim 10, wherein at least one of the plurality of jumpers is excluded from the number of jumpers in arrangement by changing from the conductive state to nonconductive state, and thereby the characteristic of the ID tag is adjusted.
12. The characteristic adjusting method of ID tag of claim 10, wherein at least one of the plurality of jumpers has electrical releasing portions missing the conductive material, and are incorporated into the number of pieces in arrangement by covering the conductive material to these electrical releasing portions, and thereby the characteristic of the ID tag is adjusted.

FIG.1

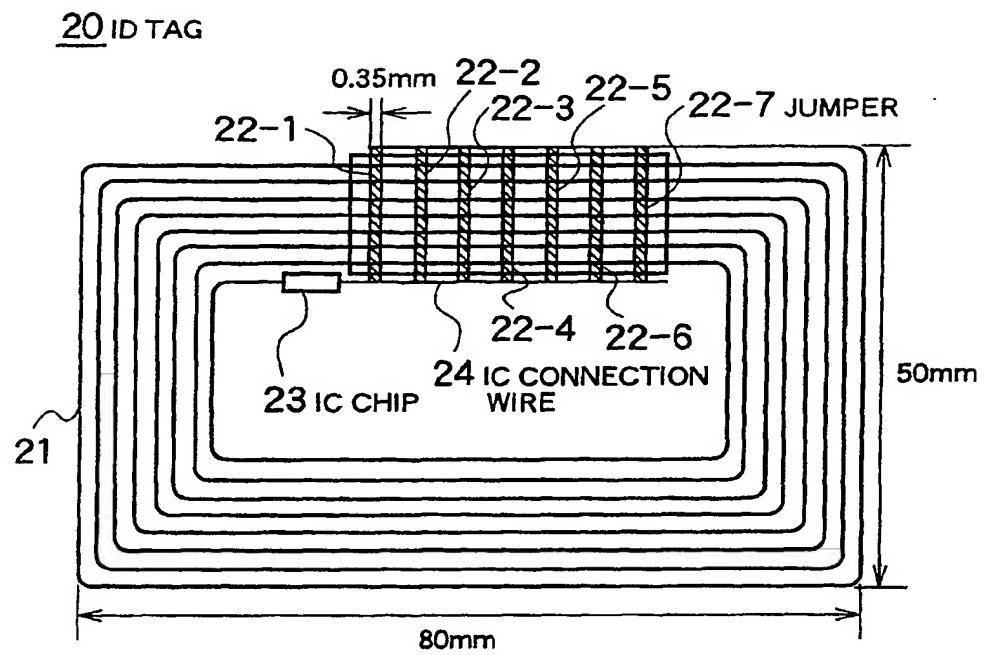


FIG.2

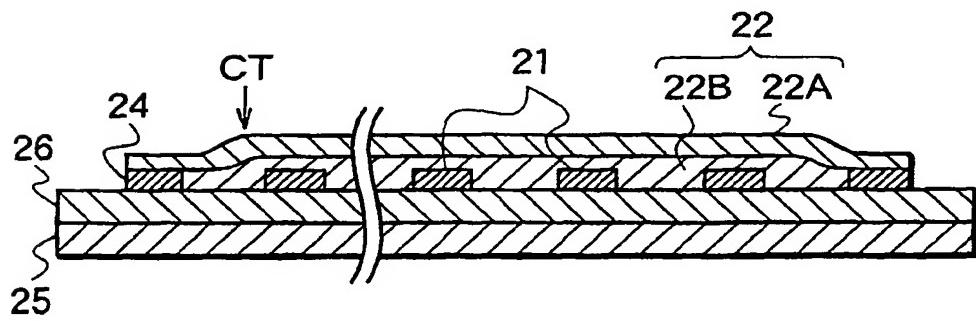


FIG.3

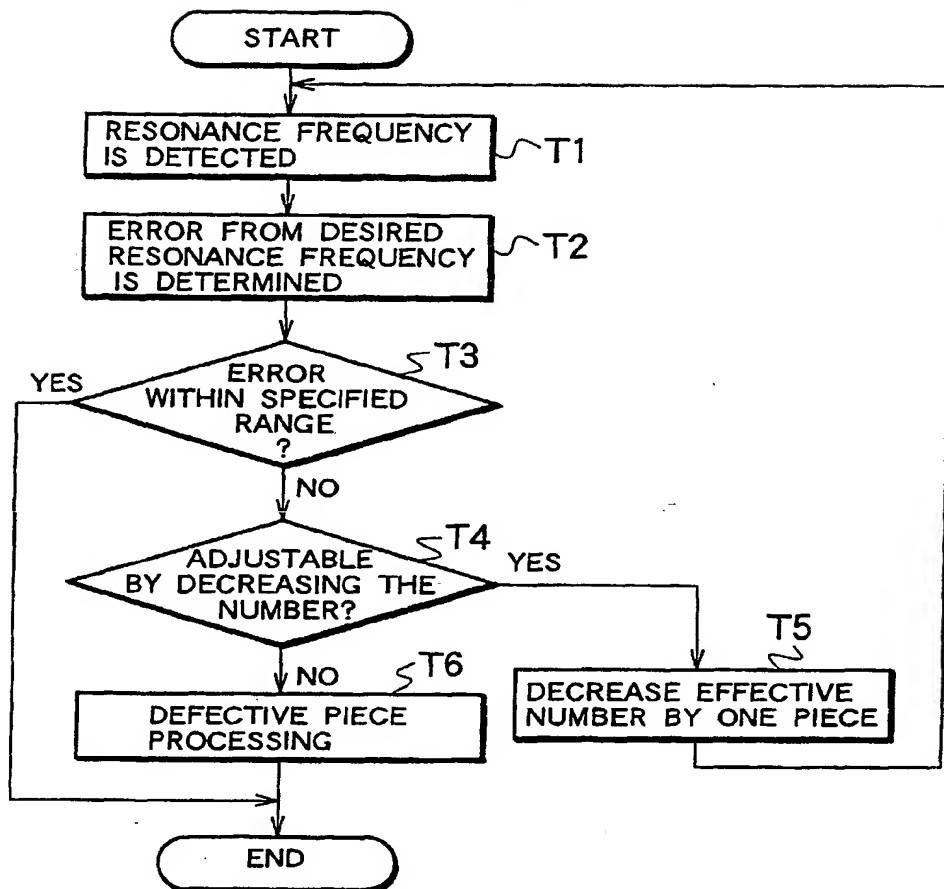


FIG.4

NUMBER OF EFFECTIVE PIECES	1	2	3	4	5	6	7
RESONANCE FREQUENCY [MHz]	14.44	14.20	13.98	13.76	13.56	13.36	13.17

FIG.5

	THICKNESS OF INSULATING LAYER [μm]	RESONANCE FREQUENCY BEFORE ADJUSTMENT [MHz]	NO. OF JUMPERS REMOVED	RESONANCE FREQUENCY AFTER ADJUSTMENT [MHz]
FIRST SAMPLE	22.0	13.177	2	13.559
SECOND SAMPLE	26.4	13.390	1	13.559
THIRD SAMPLE	26.6	13.404	1	13.568

FIG.6

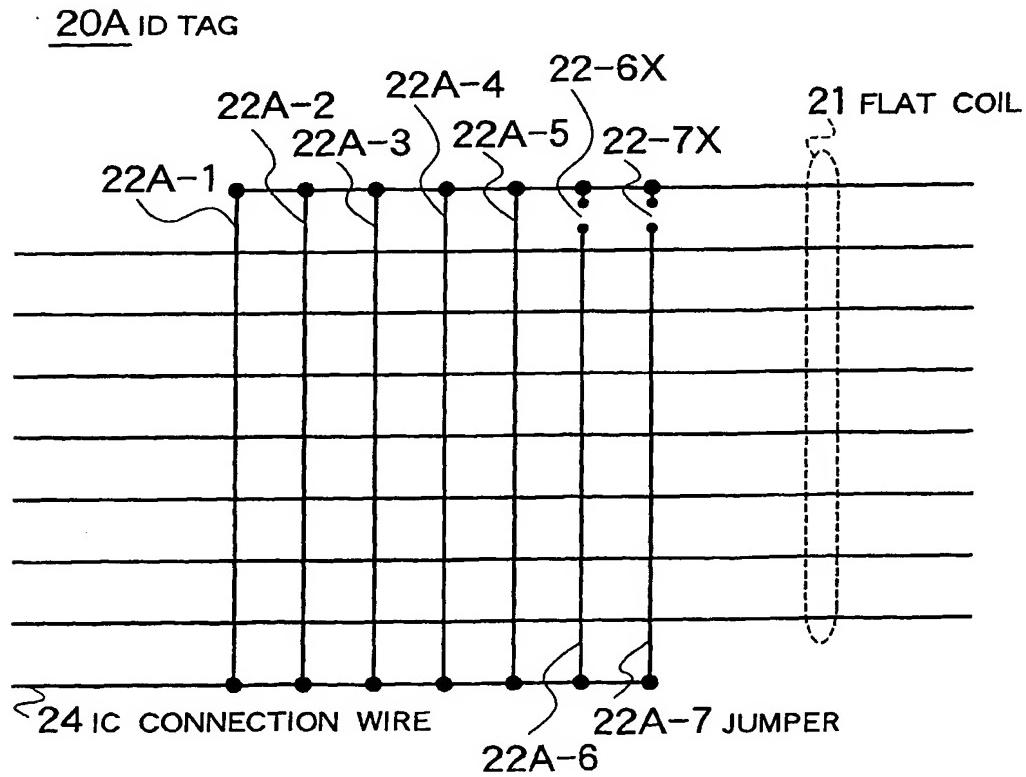


FIG.7

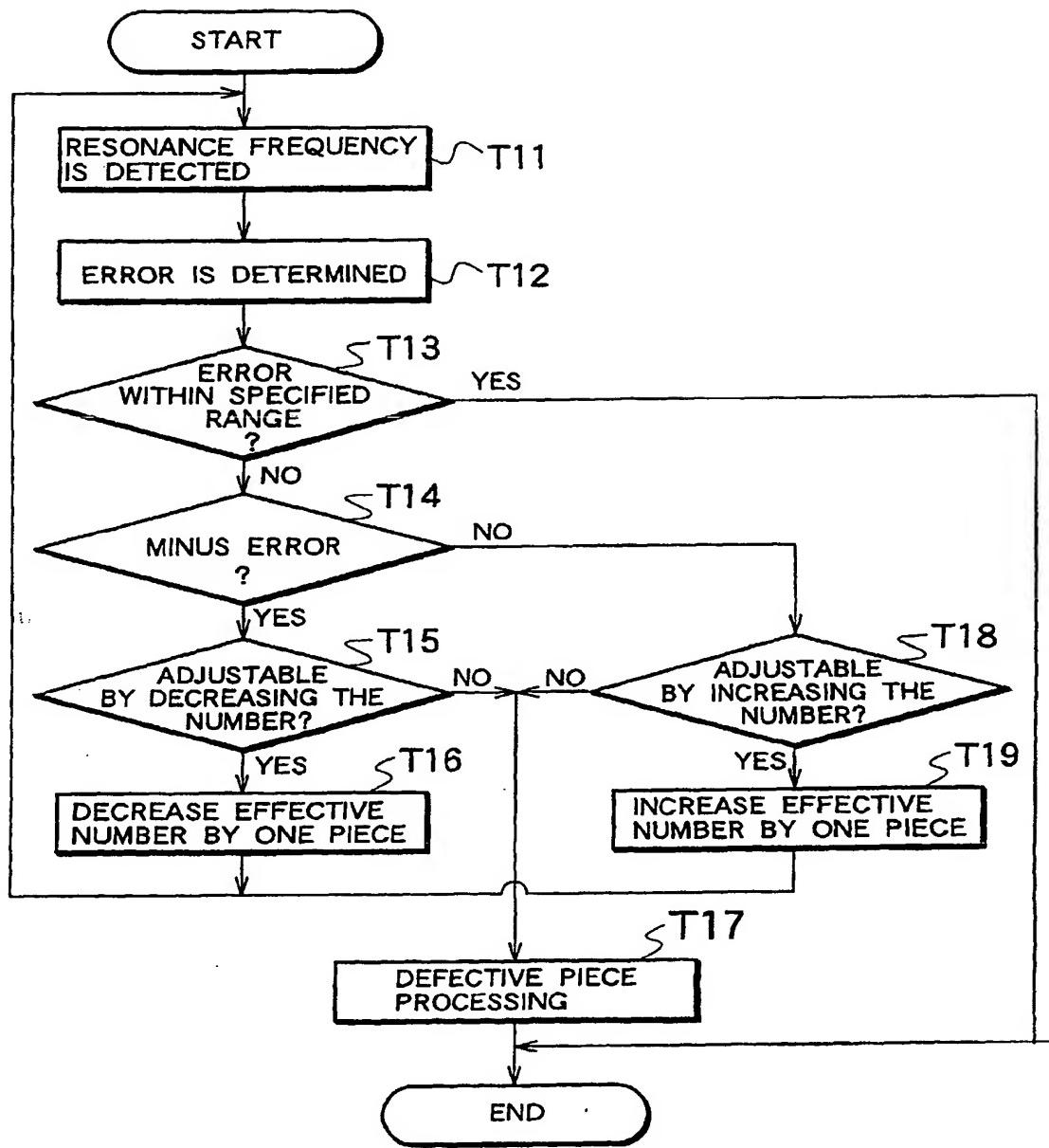


FIG.8

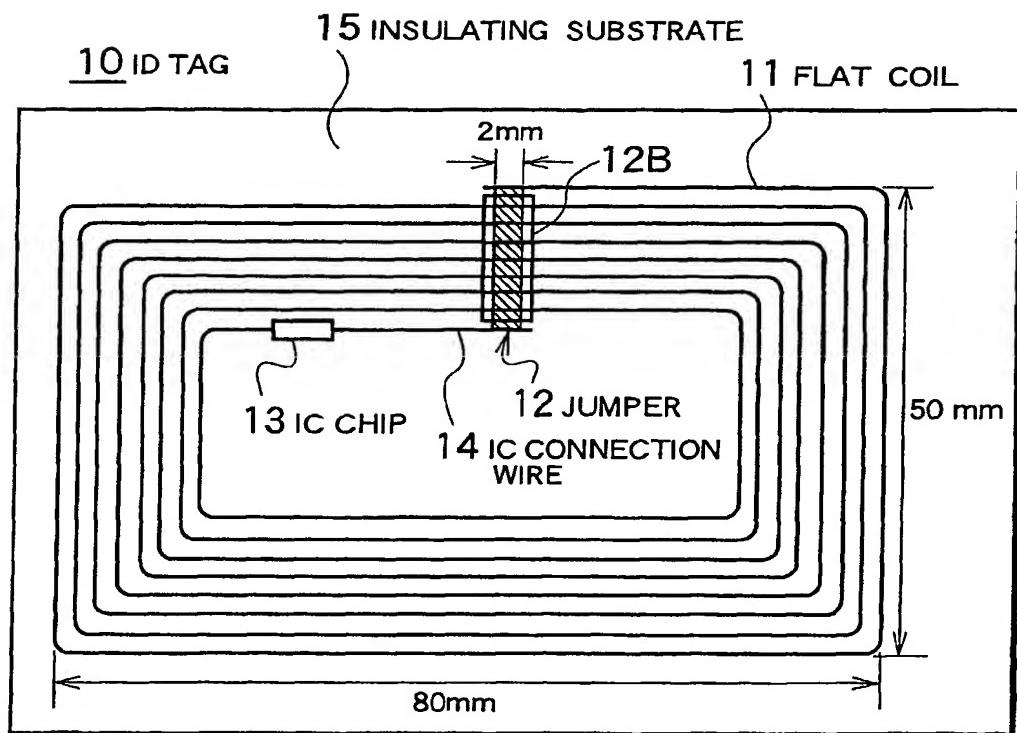


FIG.9

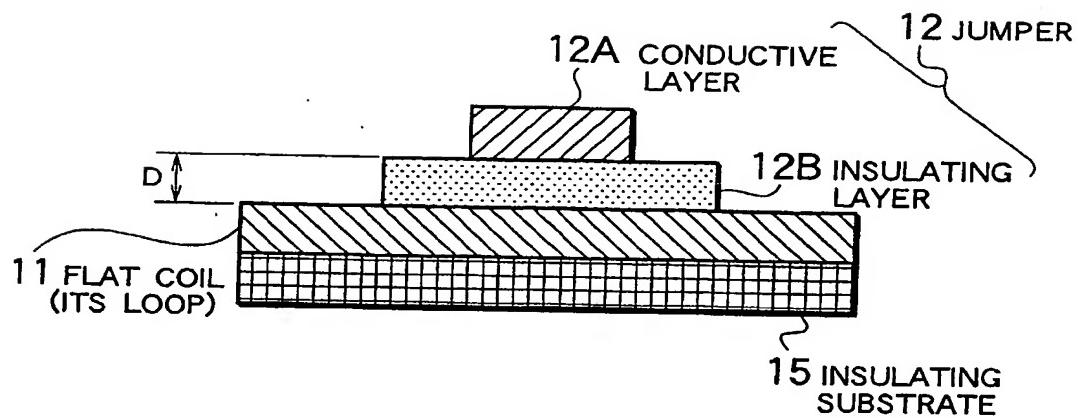
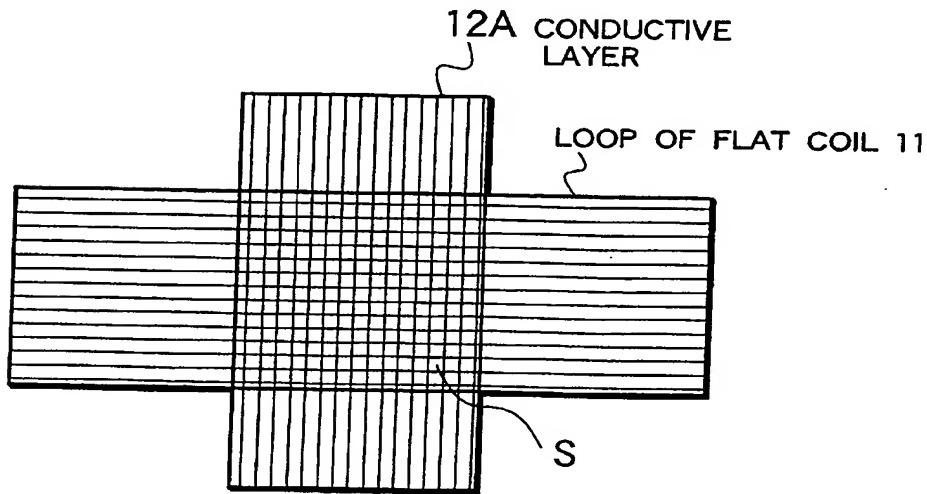


FIG.10





European Patent  
Office

## EUROPEAN SEARCH REPORT

Application Number  
EP 02 01 0842

DOCUMENTS CONSIDERED TO BE RELEVANT		Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.)		
Category	Citation of document with indication, where appropriate, of relevant passages				
D,A	PATENT ABSTRACTS OF JAPAN vol. 1999, no. 14, 22 December 1999 (1999-12-22) & JP 11 259615 A (HITACHI CHEM CO LTD), 24 September 1999 (1999-09-24) * abstract *	1	G08B13/24		
A	US 4 042 952 A (KRAYBILL ALBERT VICTOR) 16 August 1977 (1977-08-16) * column 3, line 61 - column 4, line 6; figure 1 *	1	G08B		
The present search report has been drawn up for all claims					
Place of search	Date of completion of the search	Examiner			
THE HAGUE	26 September 2002	Durville, G			
<b>CATEGORY OF CITED DOCUMENTS</b> X particularly relevant if taken alone Y particularly relevant if combined with another document of the same category A technological background D non-written disclosure P intermediate document <small>EPO/COPY 45/3281 (P09/02)</small>					
<small>I theory or principle underlying the invention</small> <small>F earlier patent document, but published on, or after the filing date</small> <small>D document cited in the application</small> <small>L document cited for other reasons</small> <small>R member of the same patent family, corresponding document</small>					

**ANNEX TO THE EUROPEAN SEARCH REPORT  
ON EUROPEAN PATENT APPLICATION NO.**

EP 02 01 0842

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26-09-2002

Patent document cited in search report		Publication date	Patent family member(s)		Publication date
JP 11259615	A	24-09-1999	NONE		
US 4042952	A	16-08-1977	CA 1082372 A1 DE 2726040 A1 FR 2354635 A1 GB 1574497 A JP 1169119 C JP 52150969 A JP 57057867 B NL 7706232 A ,B,	22-07-1980 15-12-1977 06-01-1978 10-09-1980 30-09-1983 15-12-1977 07-12-1982 13-12-1977	

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